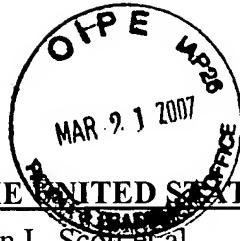


S/N 10/020,854



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Steven L. Scott et al. Examiner: Venkatesh Haliyur  
Serial No.: 10/020,854 Group Art Unit: 2616  
Filed: December 14, 2001 Docket: 499.710US1  
Title: NODE TRANSLATION AND PROTECTION IN A CLUSTERED  
MULTIPROCESSOR SYSTEM

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**INFORMATION DISCLOSURE STATEMENT**

MS RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. § 1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Serial No :10/020,854

Filing Date: December 14, 2001

Title: NODE TRANSLATION AND PROTECTION IN A CLUSTERED MULTIPROCESSOR SYSTEM

Page 2  
Dkt: 499.710US1

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications, and Non-Published Applications identifiable by USPTO Serial Number, are no longer required to be provided to the Office. Notification of this change to this effect was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004 and October 19, 2004. Thus, Applicant has not included copies of any US Patents or US Patent Applications identifiable by serial number that may be cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

STEVEN L. SCOTT ET AL.

By their Representatives,

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Date March 19, 2007

By Thomas F. Brennan

Thomas F. Brennan  
Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 19 day of March 2007.

Lisa Rosorske  
Name

Lisa Rosorske  
Signature

Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)		Complete if Known <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td>10/020,854</td> </tr> <tr> <td>Filing Date</td> <td>December 14, 2001</td> </tr> <tr> <td>First Named Inventor</td> <td>Scott, Steven</td> </tr> <tr> <td>Group Art Unit</td> <td>2616</td> </tr> <tr> <td>Examiner Name</td> <td>Haliyur, Venkatesh</td> </tr> </table>		Application Number	10/020,854	Filing Date	December 14, 2001	First Named Inventor	Scott, Steven	Group Art Unit	2616	Examiner Name	Haliyur, Venkatesh
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First Named Inventor	Scott, Steven												
Group Art Unit	2616												
Examiner Name	Haliyur, Venkatesh												
		Attorney Docket No: 499.710US1											
Sheet 1 of 1													

### US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-6,925,547	08/02/2005	Scott, S. L., et al.	12/14/2001

### FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T <sup>2</sup>

### OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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		HENNESSY, JOHN L., "Computer Organization and Design - The Hardware/Software Interface", <u>Virtual Memory</u> , (1998),593	
		KUSKIN, JEFFREY , et al., "The Stanford FLASH Multiprocessor", <u>http://www-flash.stanford.edu/architecture/papers/ISCA94/</u> , (June 6, 1996), 21 pgs.	
		PRYLLI, LOIC , et al., "An improved NIC program for high-performance MPI", <u>Workshop on Cluster-Based Computing. International Conference on Supercomputing</u> , Rhodes, Greece, (June 1999),26-30	

EXAMINER

DATE CONSIDERED